



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James P. Baukus, et al.

) Examiner: Walter Lee Lindsay Jr.

) Art Unit: 2812

Serial No.: 10/619,981

) Our Ref: B-3947 Div 621039-4

)

Filed: 07/14/2003

) Notice of Allowance dated 07/14/2004

) Confirmation No.: 7810

Title: "PROGRAMMABLE

)

CONNECTOR/ISOLATOR....

) Date: October 12, 2004

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

MAIL STOP ISSUE FEE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the Examiner amend his stated reasons on page 2, 5th paragraph of section 2, as follows:

1. "depositing a second polycrystalline silicon layer over said second insulating layer and said second buried silicon regions of the second conductivity type, ~~said second buried silicon regions of the second conductivity type~~, said second buried silicon regions of the second conductivity type being insulated from said second polycrystalline silicon layer, as required by claim 1."

We respectfully request the Examiner to make note of this obvious typographical error on the record, to thereby avoid any possible confusion regarding the true wording and scope of claim 1.

Respectfully submitted,


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by: Elizabeth Romero
Elizabeth Romero